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Applicant(s): Bryant et al.

Docket No.

BUR920010086US1

Serial No.
09/683,486Filing Date
1/7/2002Examiner
Gebremariam, Samuel A.Group Art Unit
2811Invention: FIN-BASED DOUBLE POLY DYNAMIC THRESHOLD CMOS FET WITH SPACER GATE AND
METHOD OF FABRICATIONI hereby certify that this You are hereby authorized to charge Acct. #09-0456 (IBM) \$168.00-2 extra claims
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